

L Number	Hits	Search Text	DB	Time stamp
10	104	712/236.ccls.	USPAT; IBM_TDB	2004/03/17 22:33
11	107	712/235.ccls.	USPAT; IBM_TDB	2004/03/17 22:33
12	208	712/229.ccls.	USPAT; IBM_TDB	2004/03/17 22:33
16	6	(predicate near4 register) same switch	USPAT; EPO; JPO; IBM_TDB	2004/03/17 23:01
17	43	"switch instruction" same register same (status state valid\$4)	USPAT; EPO; JPO	2004/03/17 23:01
18	67	"switch instruction" same register same (predicate one bit)	USPAT; EPO; JPO	2004/03/17 23:01
19	34	((multi-way multiway) with branch) same (status state valid\$4)	USPAT; EPO; JPO	2004/03/17 23:02
20	144	predicate near4 register	USPAT; EPO; JPO; IBM_TDB	2004/03/17 23:02
21	62	("switch instruction") same register same address	USPAT; EPO; JPO	2004/03/17 23:02
22	238	(switch near3 instruction) same (register with address)	USPAT; EPO; JPO	2004/03/17 23:02
23	5	(continuous contiguous succeeding sequential) same (predicat\$4 with register)	USPAT; EPO; JPO	2004/03/17 23:02
24	48	((multi-way multiway) with branch) same register	USPAT; EPO; JPO	2004/03/17 23:02
-	207	(switch near3 instruction) same (register with address)	USPAT	2004/03/17 23:02
-	2	("switch instruction") same address	IBM_TDB	2003/09/03 22:26
-	2	("switch instruction") same register same address	IBM_TDB	2003/09/02 17:23
-	51	("switch instruction") same register same address	USPAT	2004/03/17 23:02
-	5	("switch instruction")	IBM_TDB	2003/09/04 11:03
-	0	712/236.ccls.	IBM_TDB	2003/09/04 11:04
-	25	712/236.ccls. and switch	USPAT; IBM_TDB	2003/09/08 03:48
-	1	java near2 tableswitch	USPAT; IBM_TDB	2003/09/04 13:37
-	106	predicate near4 register	USPAT; IBM_TDB	2004/03/17 23:02
-	0	(predicate near4 register) with switch	USPAT; IBM_TDB	2003/09/04 17:43
-	18	(predicate near4 register) with branch	USPAT; IBM_TDB	2003/09/04 13:40
-	7	712/236.ccls. and predicate	USPAT; IBM_TDB	2003/09/04 17:27
-	5	(predicate near4 register) same switch	USPAT; IBM_TDB	2004/03/17 23:01
-	0	(predicate near4 register) and "switch instruction"	USPAT; IBM_TDB	2003/09/07 22:30
-	530	"switch instruction"	USPAT	2003/09/07 22:58
-	100	"switch instruction" same register	USPAT	2003/09/07 22:58
-	58	"switch instruction" same register same (predicate one bit)	USPAT	2004/03/17 23:01
-	39	"switch instruction" same register same (status state valid\$4)	USPAT	2004/03/17 23:01
-	66	712/236.ccls. and (register same (status state valid\$4))	USPAT; IBM_TDB	2003/09/07 23:01
-	37	(switch near3 instruction) same (branch with address)	USPAT	2003/09/08 03:47
-	0	((multi-way multiway) with branch) same register same (predicate)	USPAT	2003/09/10 15:59

-	42	((multi-way multiway) with branch) same register	USPAT	2004/03/17 23:02
-	3	((multi-way multiway) with branch) same predicat\$4	USPAT	2003/09/08 08:23
-	25	((multi-way multiway) with branch) same (status state valid\$4)	USPAT	2004/03/17 23:01
-	1	6571385.pn.	USPAT	2003/09/08 08:46
-	529	(continuous contiguous succeeding sequential) same (predicat\$4)	USPAT	2003/09/10 16:00
-	4	(continuous contiguous succeeding sequential) same (predicat\$4 with register)	USPAT	2004/03/17 23:02